PTO/S8/52 (06-00) Approved for use through 12/30/2000. CMB 0851-0033 U.S. Palent and Trademork Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number. 37 C.F.R. 1.175 Docket Number (optional) REISSUE APPLICATION DECLARATION BY THE ASSIGNEE

TEIOOL AT THISATION DEGLACATION D	FUJS 13.045A				
I hereby declare that:					
My residence and mailing address and citizenship are s	•				
I am authorized to act on behalf of the following assigned	e: FUJITSU LIMITED				
and the little of my position with said assignee is:	General Manager of Patent Division, Intellectual Property Departmen				
The entire title to the patent identified below is vested in	n said assignee.				
Name of Patentee(s):					
TAKANORI IWAMATSU and HIROYUKI KIYANAG	I				
Patent Number 5,867,542	Date of Patent Issued February 2, 1999				
Title of Invention	•				
LOCK PHASE DETECTING CIRCUIT AND	D CLOCK REGENERATING CIRCUTTS				
I believe said patentee(s) to be the original, first and so	le/joint inventor(s) of the subject matter which is				
described and claimed in said patent, for which a reissu	re patent is sought on the invention entitled				
(As above)					
the specification of which	•				
is attached hereto., with a c oncurently filed Preliminary Amendment					
was filed on as refssue application number/					
and was amended on(If applicable)					
I have reviewed and understand the contents of the above identified specification, including the daims, as amended by any amendment referred to above.					
I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56.					
I verily believe the original patent to be wholly or partly inoperative or invalid, for the reasons described below. (Check all boxes that apply.)					
by reason of a defective specification or drawing.					
by reason of the patentee claiming more or less than he had the right to claim in the patent.					
by reason of other errors.					
At least one error upon which reissue is based is described as follows:					
Failed to provide generic claim in original patent.					
[Attach additional she	eets, if needed.]				
All errors corrected in this reissue application arose with applicant.	rout any deceptive intention on the part of the				

[Page 1 of 2]

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REISSUE APPLICATION DECLARATION BY THE ASSIGNEE			Docks FUJ	Docket Number (Optional) FUJS 13.045A			
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Hideo OZ Signature	AWA			D-4			
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Address of Assig	duee El	JITSU LIMITED	•.		nuary 24	. 2001	
		4-chome, Nakaha		asaki -s	hi. Kana	icawa 2	11-8588 Ja
Patentee		a deadle particular	24 /(4)		zenship	IGGNA Z	11-0300 34
TAKANORI IWAMATSU		TAPAN					
Residence/Maili	•	59 ·					
Kawasaki,	Japan					•	
Pateritée				Citizenship			
HIROYUKI K Residence/Maili						APAN_	
Sendai, ja		33	•				
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Docket Number (Optional)

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REISSUE APPLICATION DECLARATION BY THE INVENTOR

FUJS 13.045A

As a below named inventor, I hereby declare that: My residence, mailing address and citizenship are stated below next to my name. I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is described and claimed in patent number 5,867,542, granted February 2, 1999, and for which a
reissue patent is sought on the invention entitled CLOCK PHASE DETECTING CIRCUIT AND CLOCK
REGENERATING CIRCUIT EACH ARRANGED IN RECEIVING UNIT
the specification of which
X is attached hereto, with a concurently filed Preliminary Amendment
was filed onas reissue application number/
and was amended on (If applicable)
I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56. I verily believe the original patent to be wholly or partly inoperative or invalid, for the reasons described below. (Check all boxes that apply.)
by reason of a defective specification or drawing.
x by reason of the patentee claiming more or less than he had the right to claim in the patent.
by reason of other errors.
At least one error upon which reissue is based is described below. If the reissue is a broadening reissue, such must be stated with an explanation as to the nature of the broadening:
Failed to provide generic claim in original patent.

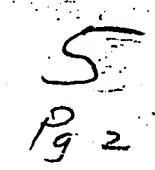
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(REISSUE APPLIC	SSUE APPLICATION DECLARATION BY THE INVENTOR, page 2) FUJS 13.045A				• • •		
applicant. As a na	ed in this reissue application arose with med inventor, I hereby appoint the follows and the United States P	wing attorney	y(s) and/o	or age	ent(s) to j	prosecute this	
Name(s) LEONARD COOP	Registration Number 27, 625						
AARON KARAS	18,923		<u> </u>				
SAMSON HELFG	OTT 23,072				 		
Correspondence A	ddress: Direct all communications about	t the application	on to:				
Customer Number Type Customer Number here Place Customer Number I Code Label here							
Firm or Individual Name	HELFGOTT & KARAS			_			
Address	EMPIRE STATE BUILDING	<u> </u>	: 		·····		
Address	350 FIFTH AVENUE - SUITE 6	024			1		
City	NEW YORK	State	NEW YO	ORK	Zip	10118	
Country	UNITED STATES OF AMERICA		Τ				
Telephone	(212) 643-5000	Fax	(212)) 643	3–2166		
the knowledge that or both, under 18 application, any particular full name of sole or	on and belief are believed to be true; and willful false statements and the like so U.S.C. 1001, and that such willful falstent issuing thereon, or any patent to where first inventor (given name, family name)	made are pu e statements ich this declar	nishable may jec	by fin pardiz	e and im ze the va	ipnsonment,	
Inventor's signature	TAKANORI IWAMATSU DateX			5			
Residence	Residence Citizensh			JAPAN			
Mailing Address KANAGAWA 21 F.	O FUJITSU LIMITED, KAMIKODA		AHARA_	KU, I	KAWASAI	KI_SHI,	
	d joint inventor (given name, family name	e)					
HIROYUKI KYANAGI							
inventor's signature	Inventor's signature		Date				
Residence Sendai	Citizenship JAPAN						
Mailing Address C 1 CHOME, AOBA	O FUJITSU TOHOKU DIGITAL KU, SENDAI-SHI, MIYAGI 980	TECHNOLOGY JAPAN	Y LTD.	2-25	ō,	1BANCHO	
	oint inventor (given name, family name)						
Inventor's signature		Date					
Residence		Citizenship					
Mailing Address							
Additional joint inve	entors are named on separately numbered sheets	attached hereto.					



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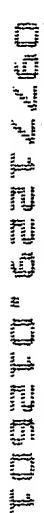
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STATEMENT UNDER 37 CFR 3.73(b)				
Applicant/Patent Owner: FUJITSU LIMITED				
Application No./Patent No.: 5,867,542	Filed/Issue Date: February 2, 1999			
Entitled: CLOCK DETECTING CIRCUIT AND CLO	XX.			
FUJITSU LIMITED, aCorpo	oration			
(Name of Accignos) (Type of Ass	signes, e.g., corperation, parinership, university, government agency, etc.)			
states that it is:	~			
1. (3) the assignee of the entire right, title, and Interest;				
2. an assignee of less than the entire right, title and The extent (by, percentage) of its ownership inter	rest is			
in the patent application/patent identified above by virtue	e or emer:			
A. [k] An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel 7778. Frame0926, or for which a copy thereof is attached.				
OR	-			
B.[] A chain of title from the inventor(s), of the patent assignee as shown below:				
1. From:	To:			
The document was recorded in the United S	States Patent and Trademark Office at, or for which a copy thereof is attached.			
,	To:			
The document was recorded in the United S	States Patent and Trademark Office at			
3. From:	Го:			
The document was recorded in the United S	States Patent and Trademark Office at, or for which a copy thereof is attached.			
[] Additional documents in the chain of title are listed on a supplemental sheet.				
[] Copies of assignments or other documents in the chain of title are attached. [NOTE: A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.08] The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.				
	Hideo OZAWA			
January 24, 2001 Date	Typed or printed name			
	Idideo Orawa			
	Signature General Manager of Patent Division, :- Intellectual Property Department			
Rustee Hour Statement: This form is estimated to take 0.2 hours to complete. This	Title			

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STATEMENT OF NON-ASSIGNMENT	FUJS 13.045A
This is part of the application for a reissue patent based on the or	riginal patent identified below.
Name of Patentee(s) TAKANORI IWAMATSU and HIROYUM	CI KIYANAGI
Patent Number 5,867,542	Date Patent Issued 02/02/99
Title of Invention CLOCK PHASE DETECTING CIRCUIT AND	D
1. Piled herein is a statement under 37 CFR 3.73(b).	(Form PTO/SB/96)
2. Ownership of the patent is in the inventor(s), and no	assignment of the patent is in effect.
One of boxes 1 or 2 above must be checked, If multiple assigned box 2 is checked, skip the next entry and go directly to "Name of The written consent of all assignees and inventors owning an une patent is included in this application for reissue.	Assignee*.
The assignee(s) owning an undivided interest in said original pate and the assignee(s) consents to the accompanying application for	
Name of assignee/inventor (if not assigned)	· · · · · · · · · · · · · · · · · · ·
FUJITSU LIMITED	
Signature	Date
Aideo Jarra	January 24, 2001
Typed or printed name and title of person signing for assignee (if	assigned)
Hideo OZAWA, General Manager of Patent Division, In	tellectual Property Department

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pursuent to 37 C.F.R. 1.10.

by / Kelesa Saunders

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